

2002 2 4 10
U.S. PATENT & TRADEMARK OFFICE

FIG. 1
BACKGROUND ART

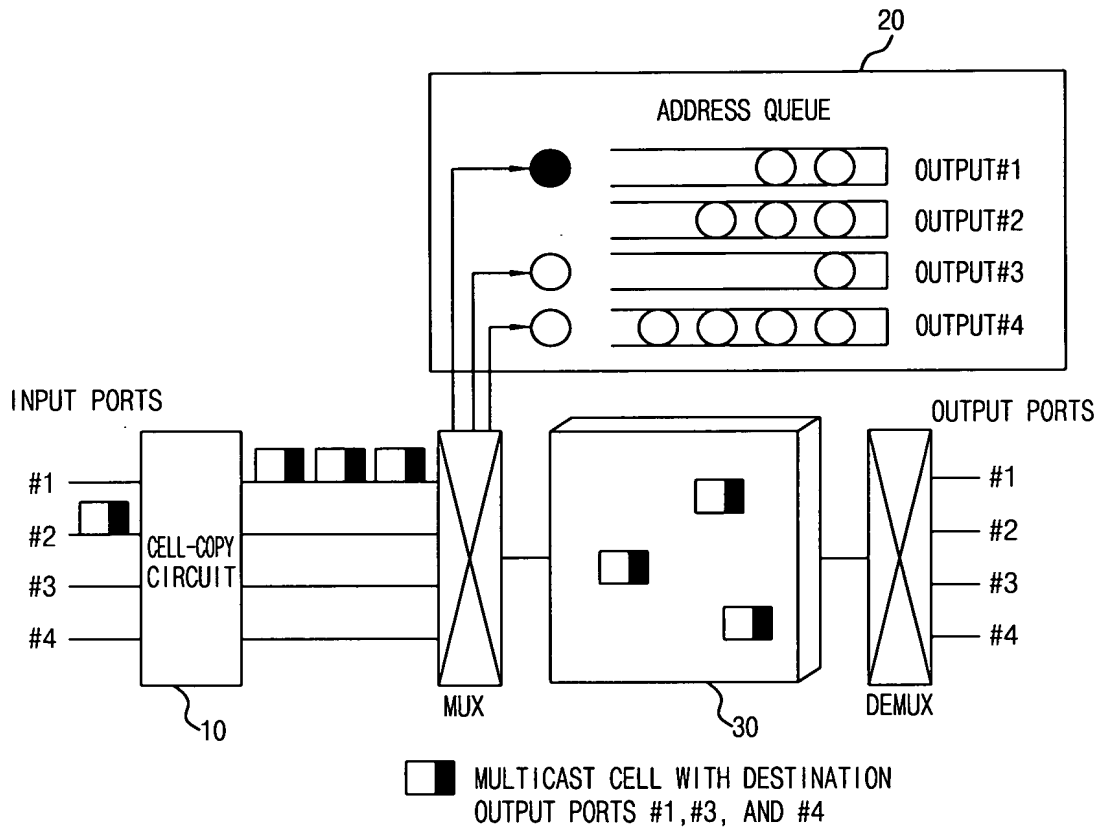


FIG. 2
BACKGROUND ART

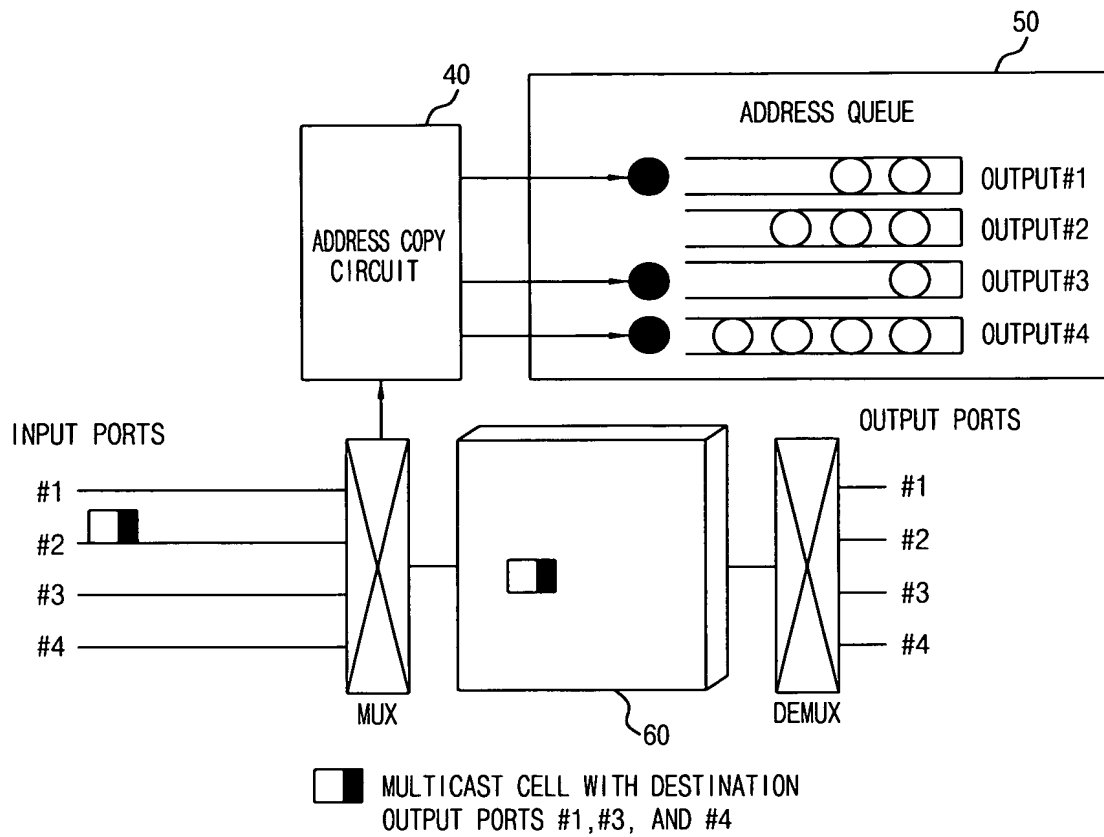


FIG. 3
BACKGROUND ART

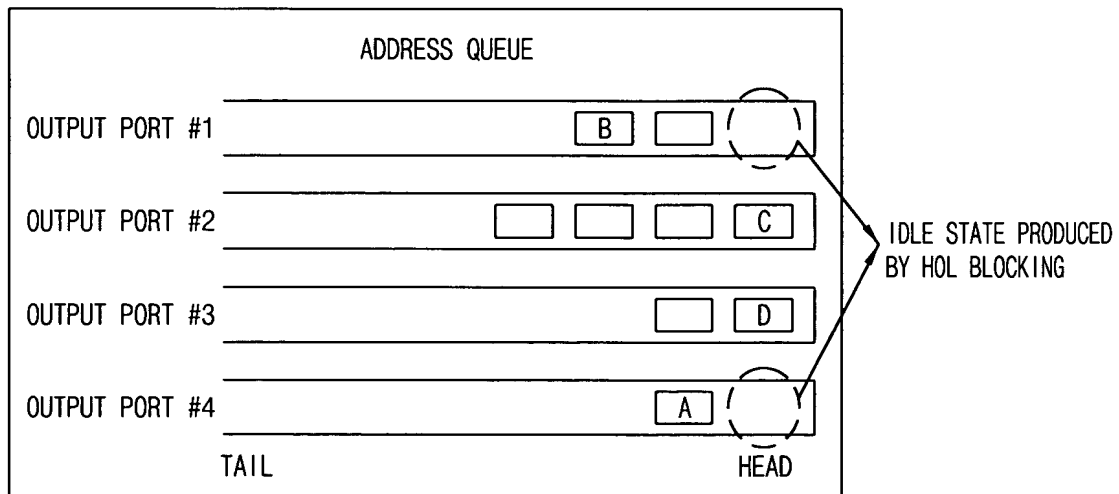
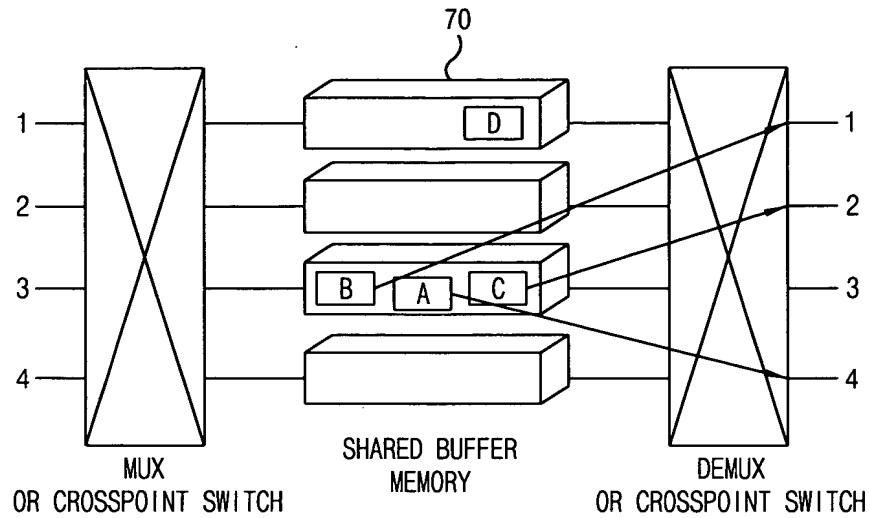




FIG.4

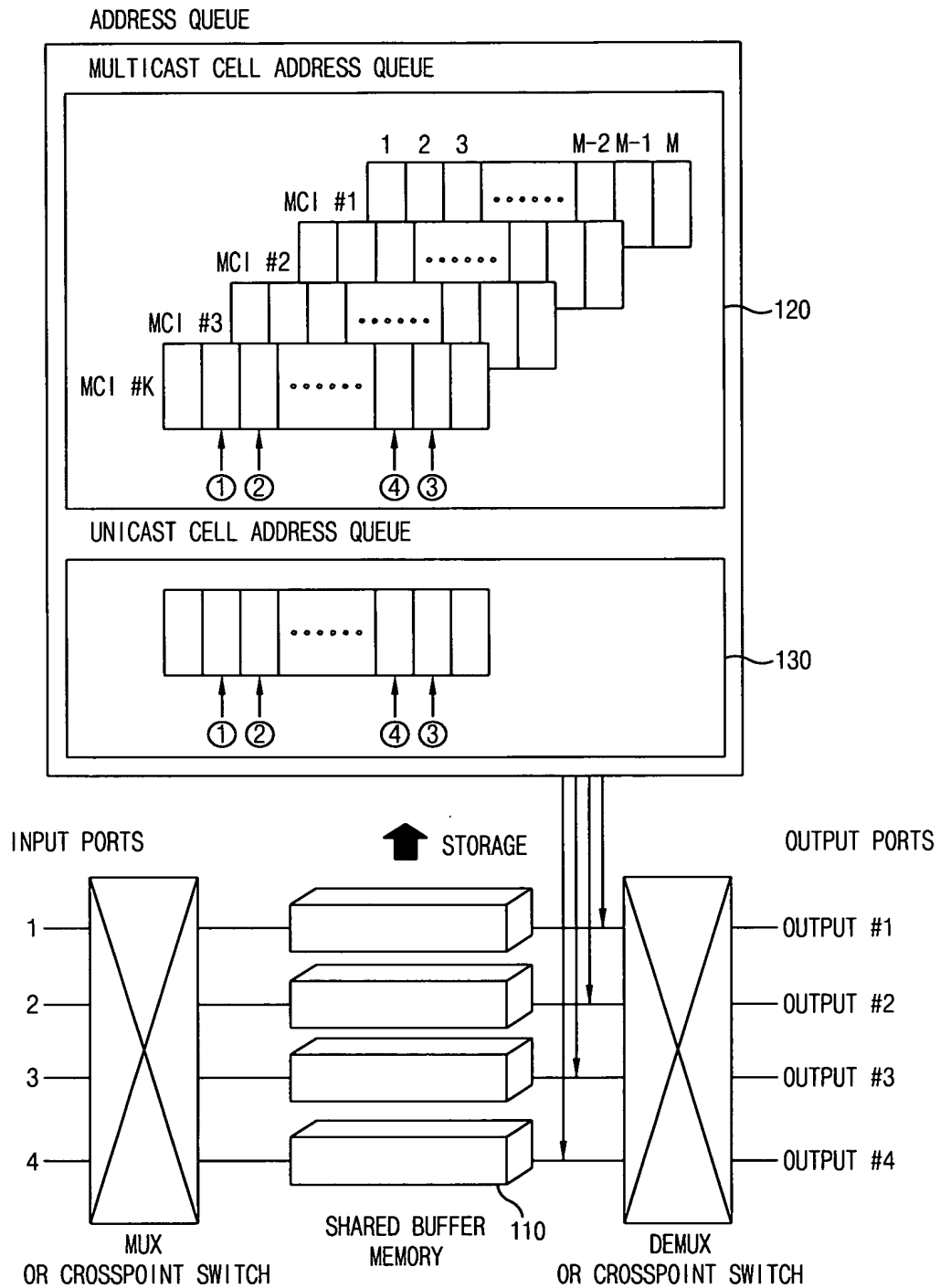




FIG.5

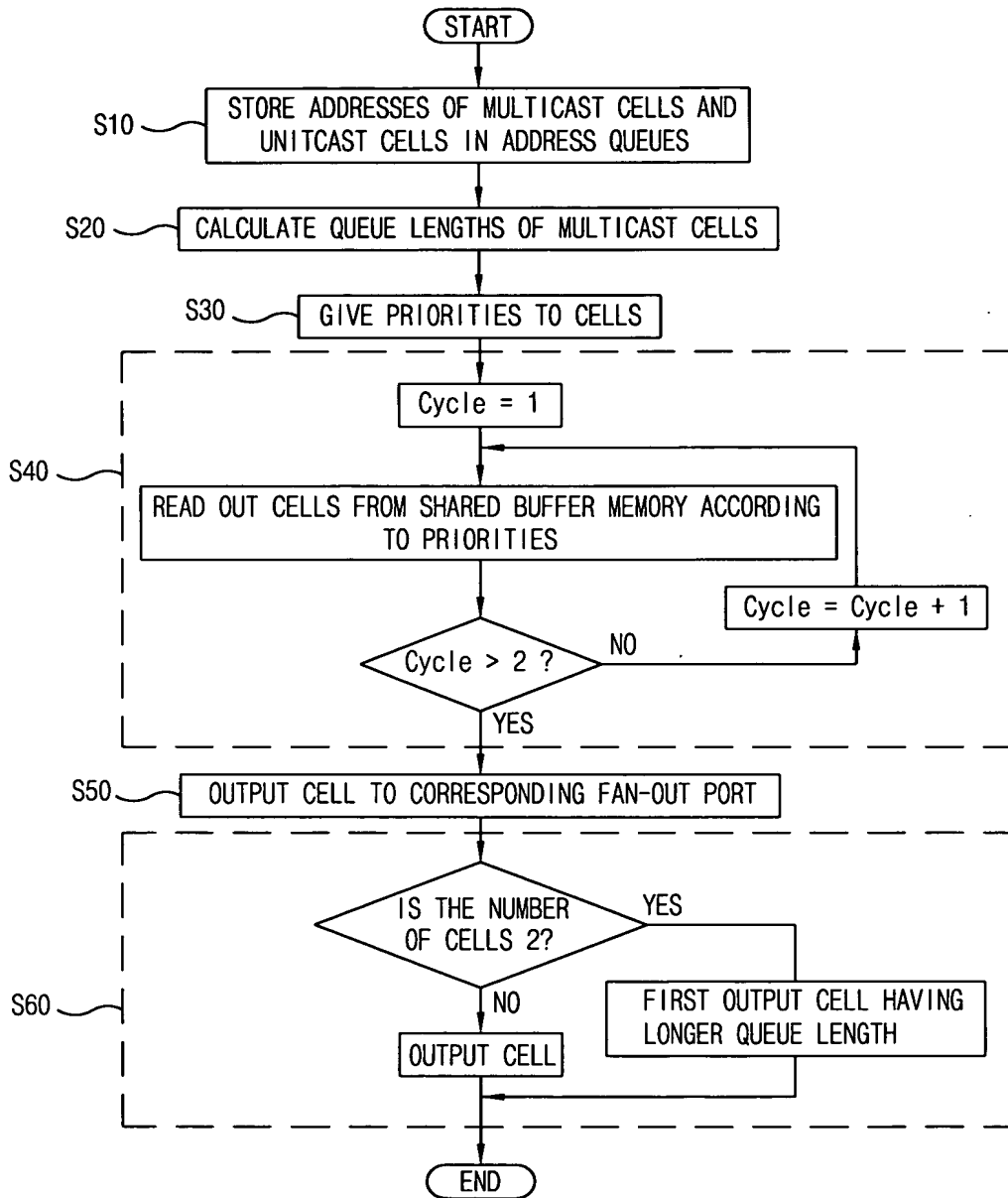


FIG.6

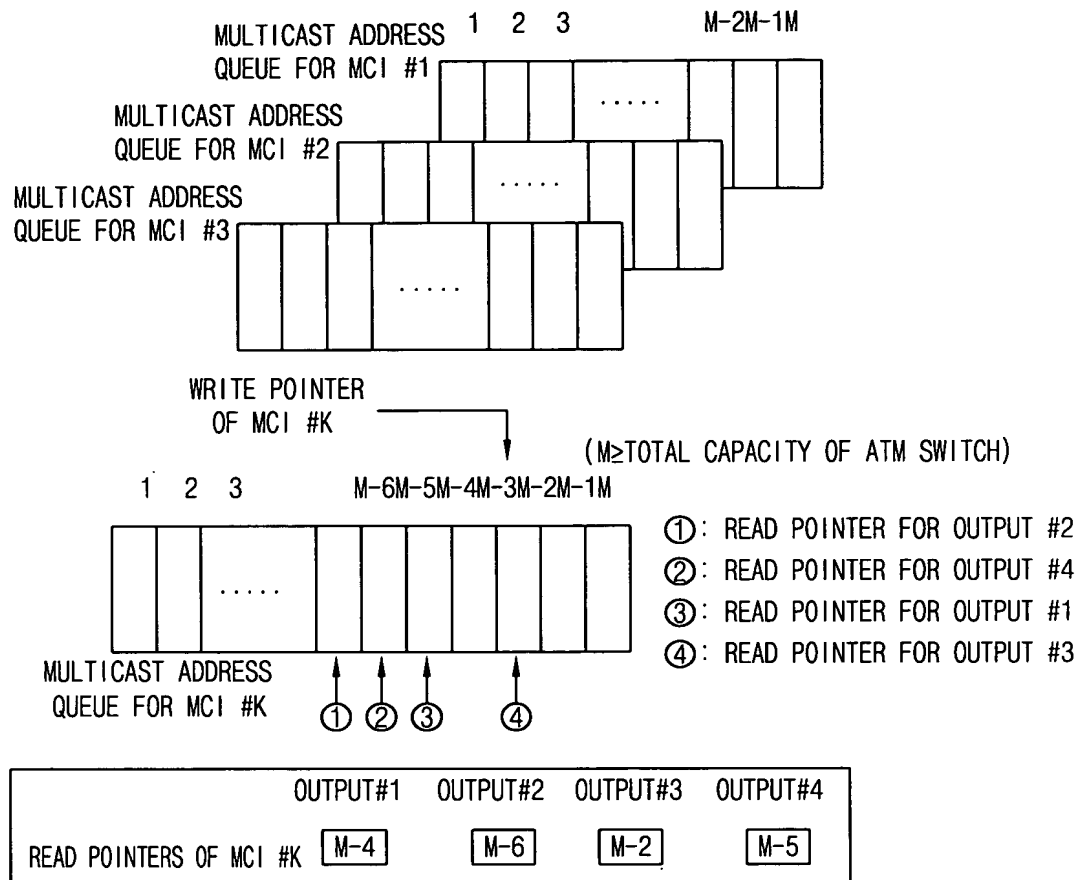


FIG.7A



	OUTPUT #1	OUTPUT #2	OUTPUT #3	OUTPUT #4
MCI #1 ADDRESS QUEUE	89	88	90	84
MCI #2 ADDRESS QUEUE	91	95	80	83
MAXIMUM QUEUE LENGTH	91	95	90	84
PRIORITY	2	1	3	4

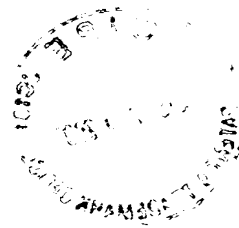


FIG.7B

OUTPUT PORT
→

	1	2	3	4
1st			SBM #4(s_1)	
2nd	SBM #2(s_2)			
3rd		SBM #2(s_3)		
4th				SBM #2(s_4)

↑
READ PRIORITY

OUTPUT PORT & READ PRIORITY OF UNICAST CELL(s)

FIG.7C

OUTPUT PORT
→

	1	2	3	4
1st		SBM #2(m_1)		
2nd	SBM #3(m_2)			
3rd			SBM #4(m_3)	
4th				SBM #4(m_4)

↑
READ PRIORITY

QUEUE LENGTH OF m_1 : 95
QUEUE LENGTH OF m_2 : 91
QUEUE LENGTH OF m_3 : 90
QUEUE LENGTH OF m_4 : 84

OUTPUT PORT & READ PRIORITY OF MULTICAST CELL(m)

FIG. 8

